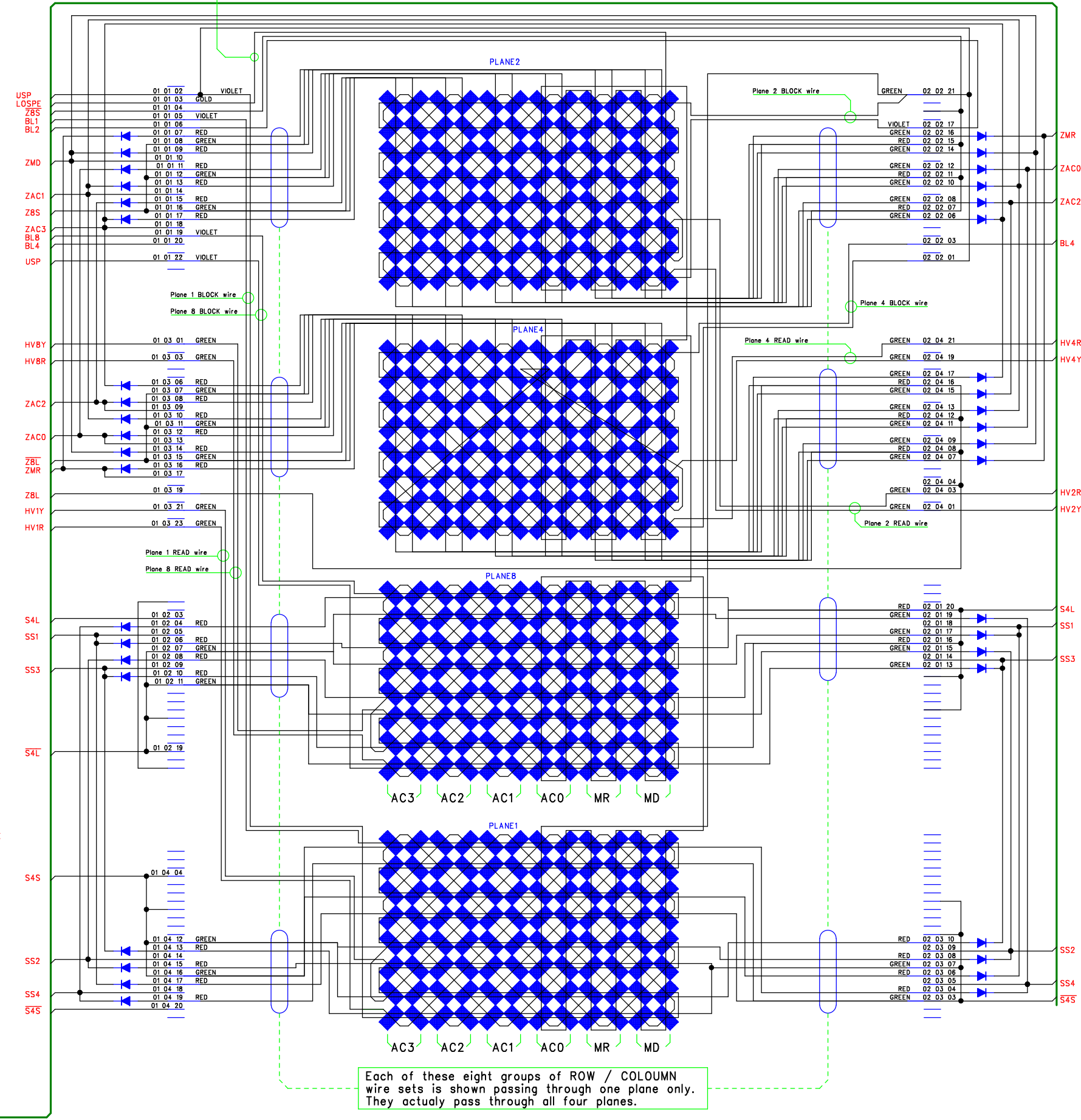
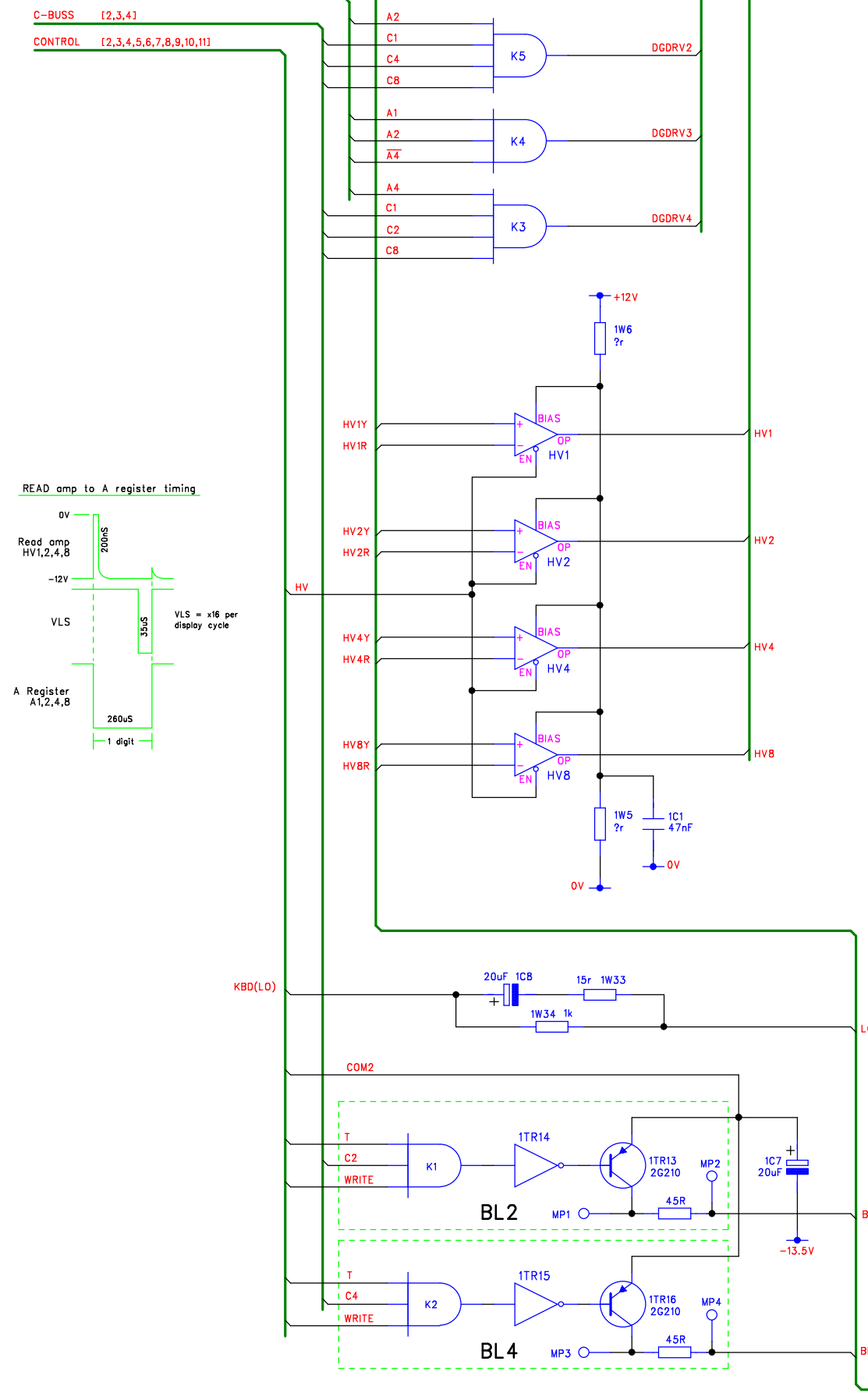


RevNo	Revision note	Date	By	Checked
-	-	-	-	-

Lö SPE only clears the MD, MR and ACO registers

- READ-BUSS [3]
- DIGIT-BUSS [3,4,9,12]
- CORE-BUSS [2]
- A-BUSS [3,4,6,7,8,9]
- C-BUSS [2,3,4]
- CONTROL [2,3,4,5,6,7,8,9,10,11]



Each of these eight groups of ROW / COLOUMN wire sets is shown passing through one plane only. They actually pass through all four planes.



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Pcb Dwg # =		Scale	Nts	Title	
Drawn	Name	Date	Sheet	Soemtron ETR 220 Calculator	
Checked	MD Hatch	21/10/2009	Next Sheet	#1 - Core Memory	
Drawing Number			Soemtron 220 Logic		
A2 Issue			A		