



1	2	3	4	5	6	7	8	9	10
A	B	C	D	E	F	G	H	J	K

RevNo	Revision note	Date	By	Checked
-	-	-	-	-

CORE-BUSS [1]
 C-BUSS [1,3,41]
 Z-BUSS
 CONTROL [1,3,4,5,6,7,8,9,10,11]
 FUNCTIONS [3,4,5,6,7,8,9,10]

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Pcb Dwg # =

Name	Date
MD Hatch	9/10/2009
Drawn	
Checked	

Scale	Nts	Title
Sheet	2	Soemtron ETR 220 Calculator
Next Sheet	3	#2 - Core Memory Drivers
Drawing Number		Logic Diagram

Issue	A
A2	10

Negative logic referenced to 0V - Logical 1 = -12v, logical 0 = 0v