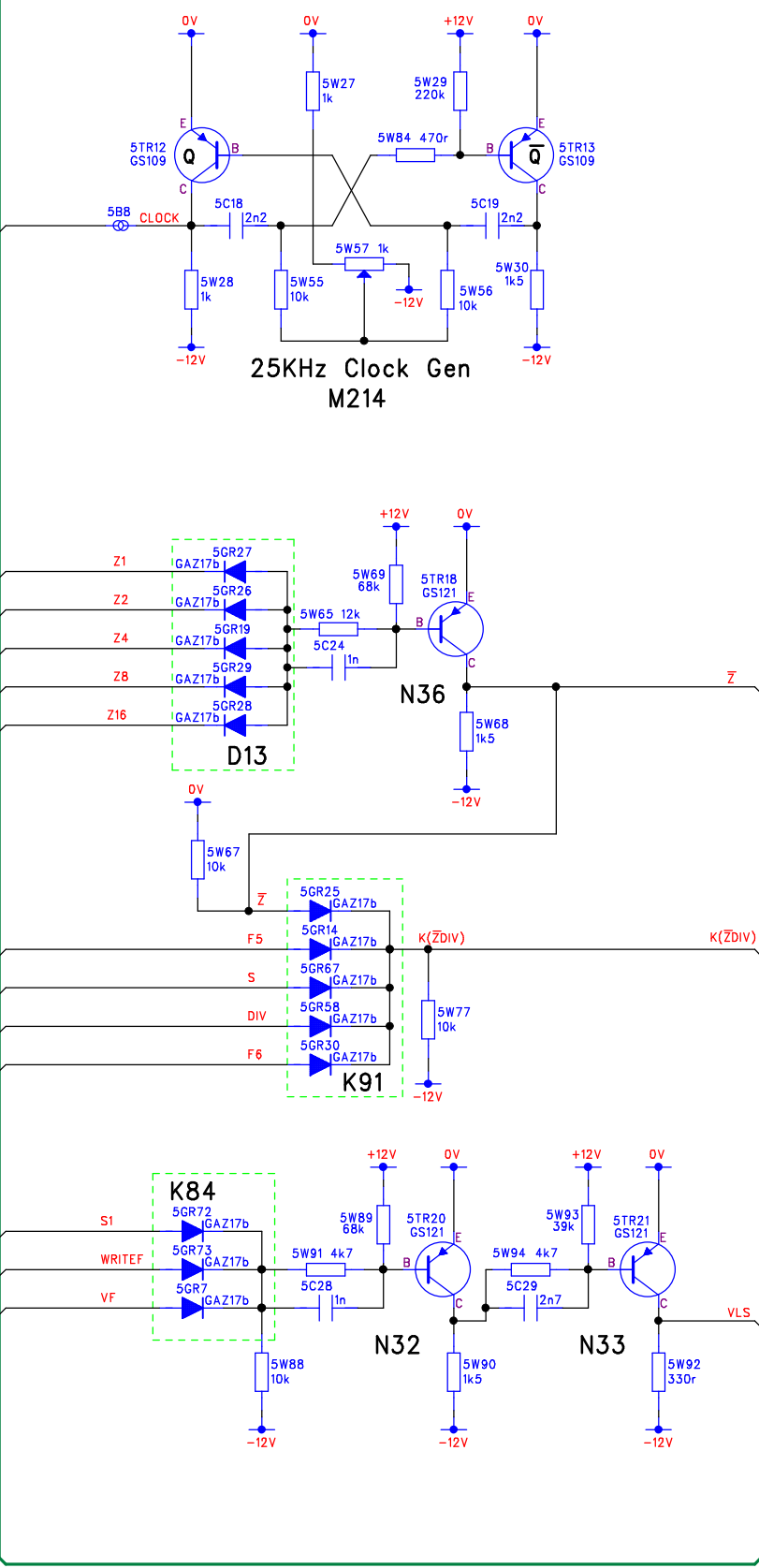
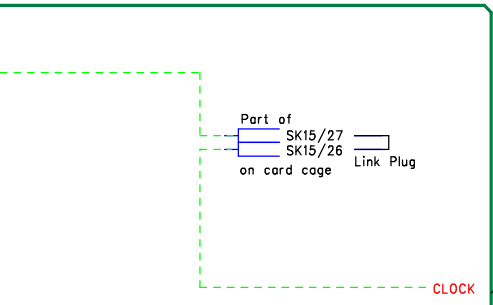
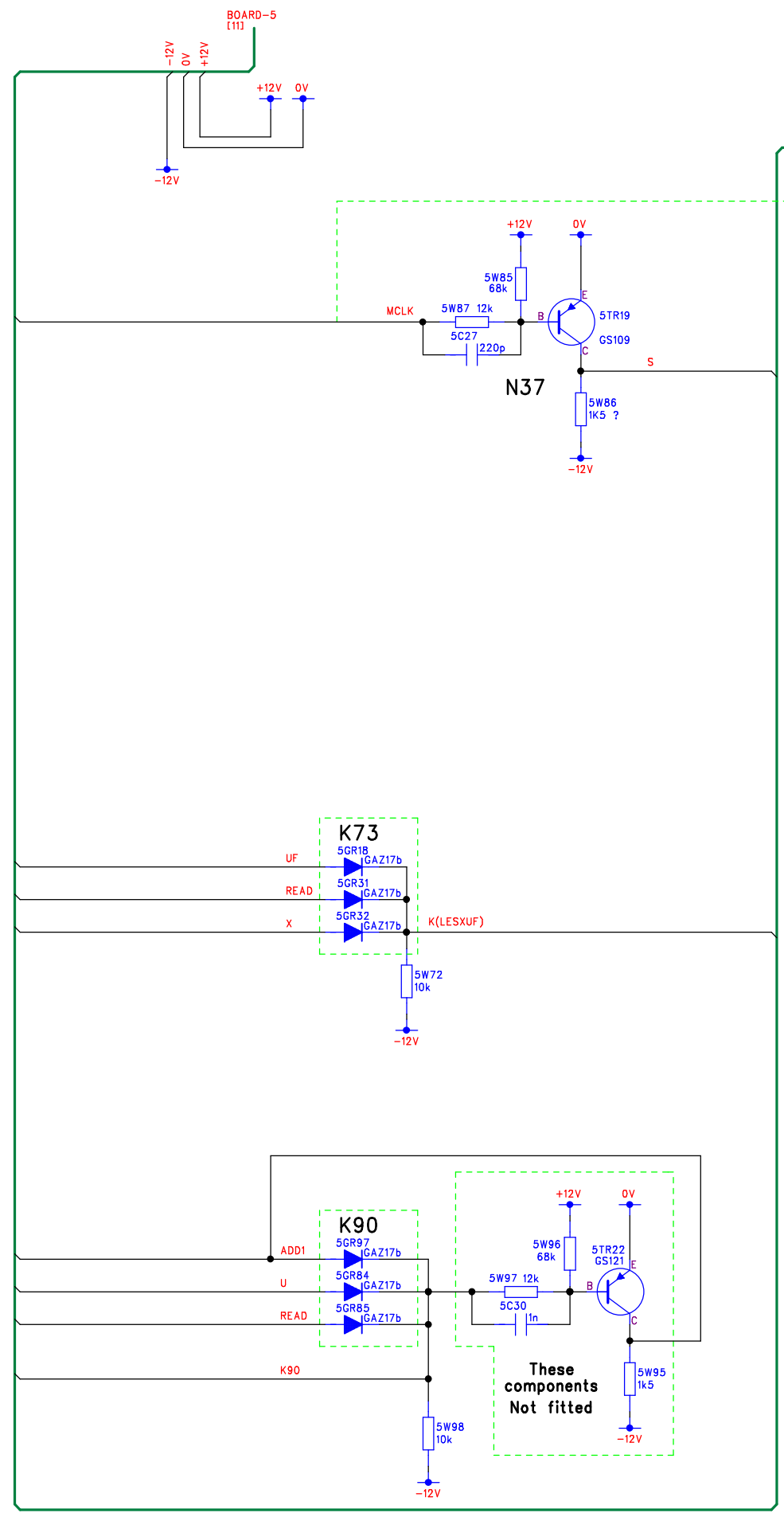


RevNo	Revision note	Date	By	Checked
-	-	-	-	-



These diagrams are referenced to 0V - Logical 1 = -12v, logical 0 = 0V



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Pcb Dwg # =

Name	Date
Drawn MD Hatch	25/2/2008
Checked -	-

Scale	Nts	Title
Sheet 12	12	Soemtron ETR 220 Calculator
Next Sheet 13	13	Control Gating - Clock Generator
Drawing Number		BOARD 5 Sheet 2
		Soemtron 220
		A2 Issue A