# **Negative-Voltage Regulators**

- 3-Terminal Regulators
- Output Current Up to 100 mA
- No External Components Required
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current Limiting
- Direct Replacement for Motorola MC79L00 Series

# OUTPUT INPUT COMMON TO-92 79L05ACZ SOT-89 79L05CPK COMMON INPUT OUTPUT

### description

This series of fixed negative-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition,

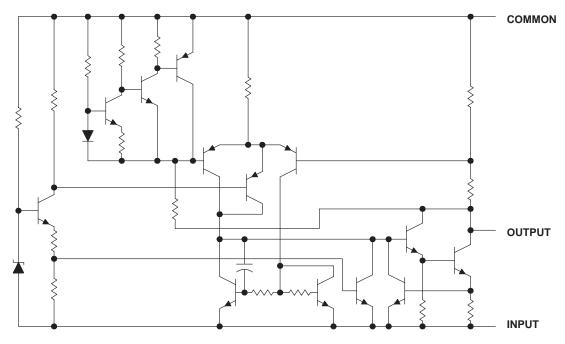
they can be used to control series pass elements to make high-current voltage-regulator circuits. One of these regulators can deliver up to 100 mA of output current. The internal current-limiting and thermal-shutdown features make them essentially immune to overload. When used as a replacement for a zener-diode and resistor combination, these devices can provide effective improvement in output impedance of two orders of magnitude, with lower bias current.

electrical characteristics at specified virtual junction temperature,  $V_I$  =-10 V, I = 40 mA (unless otherwise noted)

PARAMETER	TEST CONDITIONS	т‡	79L05			UNIT
			MIN	TYP	MAX	
Output voltage		25°C	-4.8	-5	-5.2	
	Io=1mA to 40mA,Vi=-7V to -20V	Full range	-4.75		-5.25	V
	I <sub>O</sub> = 1 mA to 70 mA	Full range	-4.75		-5.25	
Input voltage regulation	V <sub>I</sub> = -7V to -20V	25°C		15	150	mV
	V <sub>I</sub> = -8V to -20V				100	
Ripple rejection	V <sub>I</sub> = -8V to -18V f = 120 Hz	25°C	41	49		dB
Output voltage regulation	I <sub>O</sub> = 1 mA to 100 mA	25°C		20	60	mV
	I <sub>O</sub> = 1 mA to 40 mA			10	30	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		40		μV
Dropout voltage		25°C		1.7		V
Bias current		25°C			6	
		125°C			5.5	mA
Bias current change	V <sub>I</sub> = -8V to -20V	Fullrance			1.5	
	I <sub>O</sub> = 1 mA to 40 mA	- Full range			0.1	mA

<sup>‡</sup> Pulse-testing techniques maintain T<sub>J</sub> as close to T<sub>A</sub> as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output. Full range for the 79L05 is T<sub>J</sub> = 0°C to 70°C

# equivalent schematic



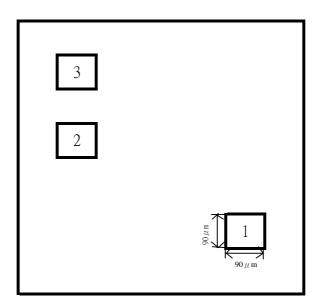
## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Input voltage: 79L05		30V
Operating free-air, case, or	virtual junction temperature	150 °C

### recommended operating conditions

79L05	MIN	MAX	UNIT
Input voltage, V <sub>I</sub>	-7	-20	V
Output current, IO		100	mA
Operating virtual junction temperature, TJ		70	°C

Pad Location WS79L00



chip size 1.15 x 1.35mm

# **Pad Location Coordinates**

Pad N	Pad Name	X( μ m)	Υ( μ m)
1	Ground	1150	115
2	Input	115	690
3	Output	115	950

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.