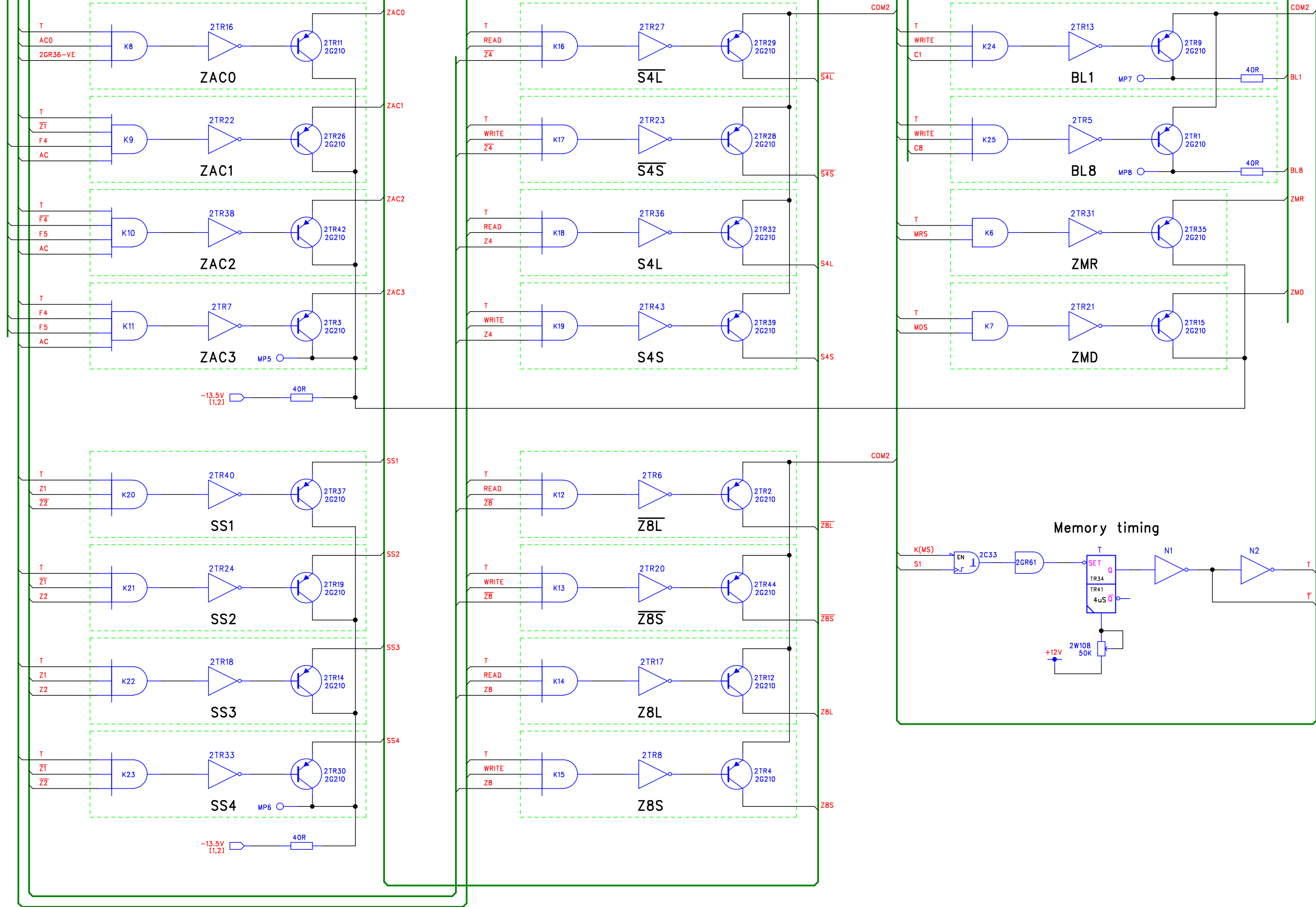
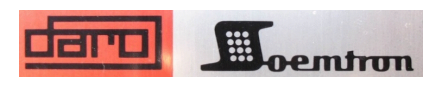


CORE-BUSS [1]
 C-BUSS [1,3,4]
 Z-BUSS
 CONTROL [1,3,4,5,6,7,8,9,10,11]
 FUNCTIONS [3,4,5,6,7,8,9,10]

RevNo	Revision note	Date	By	Checked
-	-	-	-	-



Negative logic referenced to 0V - Logical 1 = -12v, logical 0 = 0v



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Name	Date
Drawn MD Hatch	9/10/2009
Checked -	-

Scale	Nts
Sheet 2	2
Next Sheet	3
Drawing Number	

Title	Issue
Soemtron ETR 220 Calculator #2 - Core Memory Drivers Logic Diagram	A